## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of: MORIN ET AL.

Serial No. Not yet assigned

Filing Date: Herewith

For: SEMICONDUCTOR DEVICE WITH MOS )
TRANSISTORS WITH AN ETCH-STOP )

LAYER HAVING AN IMPROVED )
RESIDUAL STRESS LEVEL AND )
METHOD FOR FABRICATING SUCH A )

SEMICONDUCTOR DEVICE

I HEREBY CERTIFY THIS PAPER OR FEE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED BELOW AND IS ADDRESSED TO: MS PATENT APPLICATION, PO BOX 1450, ALEXANDRIA, VA 22313-1450.

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## PRELIMINARY AMENDMENT

MS Patent Application Commissioner For Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

• مدرونا

Prior to the calculation of fees and examination of the present application, please enter the amendments and remarks set out below.

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## In the Drawings:

Attached is one (1) replacement drawing sheet. The changes made to the drawings are explained in the remarks section below.